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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Masaki YAMADA, et al.

SERIAL NO: 10/003,114

GAU: 2826

FILED: December 6, 2001

EXAMINER: MANDALA, VICTOR A.

FOR: SEMICONDUCTOR DEVICE HAVING BURIED CONDUCTIVE LAYER FORMED IN AN INSULATING LAYER AND METHOD OF MANUFACTURING THEREOF

REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

This is a request for Continued Examination (RCE) under 37 C.F.R. §1.114 of the above-identified application.

Submission required under 37 C.F.R. §1.114

Previously Submitted:

- ☐ Consider the amendment(s)/reply under 37 C.F.R. §1.116 previously filed on
☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on

Enclosed:

- ☒ Amendment/Reply
☒ Information Disclosure Statement (IDS)
☒ Other: Form PTO-1449, Korean Office Action w/English Translation, Cited Reference (1)

FEES	RATE	CALCULATIONS
<input type="checkbox"/> Suspension of action on the above-identified application is requested under 37 C.F.R. §1.103(c) for a period of months.	\$200.00	\$0.00
<input checked="" type="checkbox"/> RCE Fee required under 37 C.F.R. §1.17(e)	\$790.00	\$790.00
<input type="checkbox"/>		\$0.00
<input type="checkbox"/>		\$0.00
TOTAL OF ABOVE CALCULATIONS:		\$790.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING AS SMALL ENTITY		\$0.00
TOTAL:		\$790.00

- ☐ A check in the amount of \$0.00 is enclosed
- ☒ Credit card payment form is attached to cover the fees in the amount of \$790.00
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 CFR 1.136, and any additional fees required under 37 CFR 1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate of this sheet is enclosed.

05/05/2005 TBESHAH1 00000056 10003114

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790.00 OP

Respectfully Submitted,

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DOCKET NO: 216932US-2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
MASAKI YAMADA, ET AL. : EXAMINER: MANDALA, VICTOR A.
SERIAL NO: 10/003,114 :
FILED: DECEMBER 6, 2001 : GROUP ART UNIT: 2826
FOR: SEMICONDUCTOR DEVICE :
HAVING BURIED CONDUCTIVE LAYER
FORMED IN AN INSULATING LAYER
AND METHOD OF MANUFACTURING
THEREOF

AMENDMENT FILED CONCURRENTLY

WITH REQUEST FOR CONTINUED EXAMINATION (RCE)

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action of February 16, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.